

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: David E. McKinley
Assignee: Zilog, Inc.
Title: "DYNAMIC POWER MANAGEMENT OF SOLID STATE MEMORIES"
Serial No.: 07/910,213 Filed: July 9, 1992
Examiner: G. Davis Group Art Unit: 2308
Attorney Docket No.: M-2689 US

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AMENDMENT AFTER FINAL OFFICE ACTION

Sir:

In response to the Final Office Action mailed August 4, 1994, please amend the above-identified patent application as set forth below.

IN THE CLAIMS

1. (Twice Amended) A dynamic power management device for supplying power to a solid state memory integrated circuit in an electronic system, said device comprising:

power control means for supplying a variable voltage to said memory integrated circuit [independently from the] without varying a voltage being supplied to elements [the rest] of said electronic system other than said memory integrated circuit; and

logic control means for generating address and control signals for said memory integrated circuit and for controlling said power control means to supply power to said memory integrated circuit at a level to maintain memory information in said memory integrated circuit during periods of no data access activity and supplying

LAW OFFICES OF
SKIERVEN, MORRILL,
HARRISON, FRANKLIN
& FINELL

35 MITCHELL DRIVE
SUITE 700
SAN JOSE, CA 95110
(408) 283-1222
FAX (408) 283-1323

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